

## **Memory consumption**

- · Shared nothing
  - Heroic theory
  - In practice: Some data is duplicated

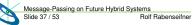
#### MPI & OpenMP

With n threads per MPI process:

- Duplicated data is reduced by factor n
- Future:

With 100+ cores per chip the memory per core is limited.

- Data reduction though usage of shared memory may be a key issue
- No halos between



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# Chances, if MPI speedup is limited due to *"algorithmic"* problems

- Algorithmic chances due to larger physical domains inside of each MPI process
  - $\rightarrow$  If multigrid algorithm only inside of MPI processes
  - $\rightarrow$  If separate preconditioning inside of MPI nodes and between MPI nodes
  - $\rightarrow$  If MPI domain decomposition is based on physical zones

# How many multi-threaded MPI processes per SMP node

- SMP node = 1 Chip
  - 1 MPI process per SMP node
- SMP node is n-Chip ccNUMA node
  - With x NICs (network interface cards) per node
- How many MPI processes per SMP node are optimal?
  - somewhere between 1 and n

#### In other words:

- How many threads (i.e., cores) per MPI process?
  - Many threads
    - → overlapping of MPI and computation may be necessary,
      → some NICs unused?
  - Too few threads
     → too much memory consumption (see previous slides)

Message-Passing on Future Hybrid Systems Slide 38 / 53 Rolf Rabenseifner

## **Further Chances**

- Reduced number of MPI messages, reduced aggregated message size
   Compared to pure MPI
- Functional parallelism
  - $\rightarrow$  e.g., I/O in an other thread
- MPI shared memory fabrics not loaded if whole SMP node is parallelized with OpenMP

Rolf Rabenseifne









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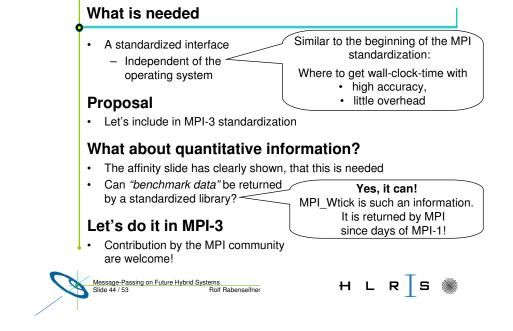
#### Aspects & Outline Which hardware topology information Future High Performance Computing (HPC) Structure of the cluster and memory hierarchy $\rightarrow$ always hierarchical hardware design Data exchange "speed" • Mismatches and chances with current MPI based (e.g., transmission time for a given data size) programming models $\rightarrow$ Some new features are needed $\rightarrow$ e.g., OpenMP subteams $\rightarrow$ Some optimizations can be done best by the application itself $\rightarrow$ e.g., hardware topology information Optimization always requires knowledge on the hardware: → Qualitative and quantitative information is needed → through a standardized interface Impact of current software and benchmark standards → on future hardware & software development $\rightarrow$ They may exclude important aspects The MPI-3 Forum tries to address those aspects → MPI-2.1 is only a starting point: Message-Passing on Future Hybrid Systems Slide 42 / 53 Bolt Rabenseifner HLRS combination of MPI-1.1 and 2.0 in one book

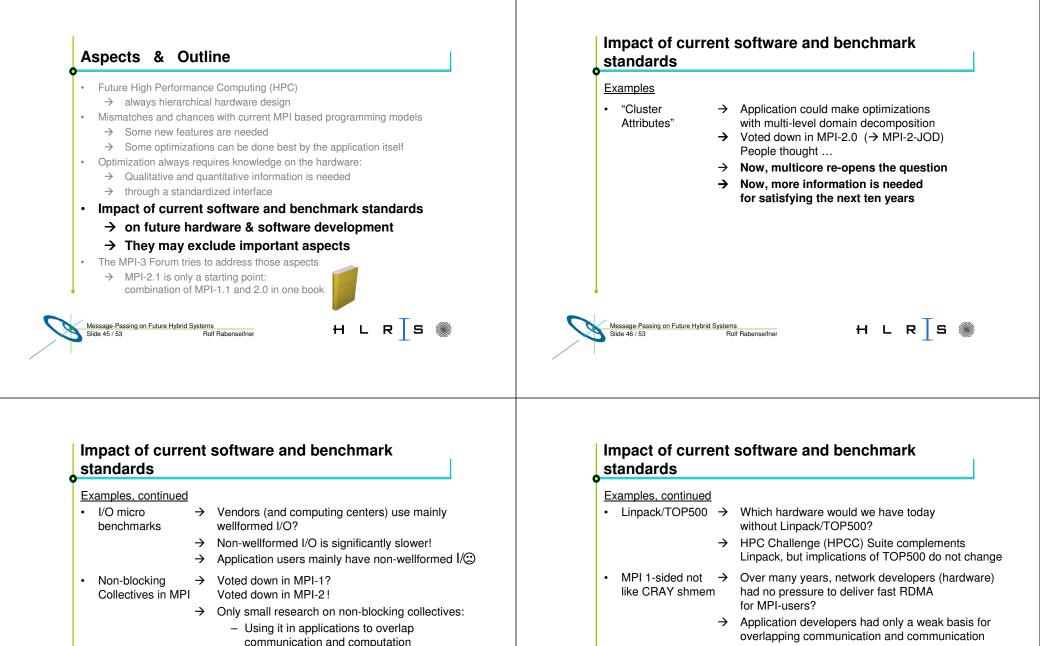
## Where to get this information

· Currently, this information is accessible through different interfaces

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- E.g., numalib / numctl
- Linux processor information
- ...
- Most information must be measured by the application





→ Compiler writers had lack of a standardized basis for the combination of PGAS and MPI (no common standardized RDMA interface)



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### Sparse non-blocking collectives in quantum mechanical calculations. Tuesday, 15:15-15:45 Communication optimization for medical image

→ latency hiding of all MPI Allreduce calls

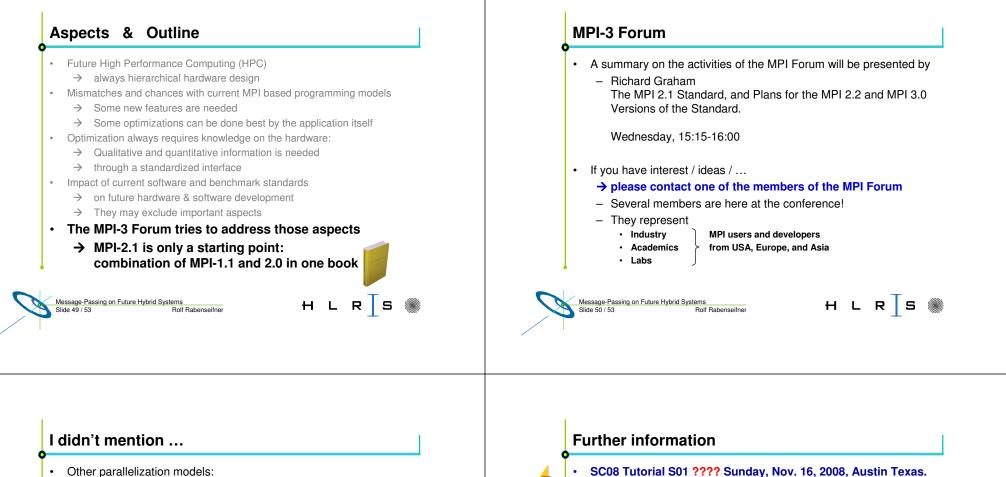
reconstruction algorithms. Tue. 16:15-16:45

- Implementing collectives in the NIC

→ The MPI-3 Forum may be responsible for

another 10 years !?

→ Torsten Hoefler at al.:



Alice Koniges, David Eder, Bill Gropp, Ewing (Rusty) Lusk, and Rolf Rabenseifner:

Application Supercomputing and the Many-Core Paradigm Shift.

MPI-2.1 (June 23, 2008 – finally voted at MPI Forum meeting, Sep. 4, 2008)

As hardcover book (608 pages) at EuroPVM/MPI'08 registration-desk:

If a colleague of you wants also the book, you should organize it now & here !

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SC08 Tutorial M09, Monday, Nov. 17, 2008, Austin Texas.

 Sold at costs - 17 Euro - cash only (you get a receipt) · Available only at some events - for you probably only here.

Rolf Rabenseifne

Rolf Rabenseifner, Georg Hager, Gabriele Jost, and Rainer Keller: Hybrid MPI and OpenMP Parallel Programming.

Electronically via www.mpi-forum.org

 The book was printed by HLRS As a service for the MPI community. High-quality sewn binding.

• Not via normal book stores!

Message-Passing on Future Hybrid Systems

- Other parallelization models:
  - Partitioned Global Address Space (PGAS) languages (Unified Parallel C (UPC), Co-array Fortran (CAF), Chapel, Fortress, Titanium, and X10).

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- High Performance Fortran (HPF)
- → Many rocks in the cluster-of-SMP-sea do not vanish into thin air by using new parallelization models
- $\rightarrow$  Area of interesting research in the next years



### Conclusions

