






## NEC SX-8 Course — 1st day (Wednesday, April 6, 2005)

### NEC systems at HLRS

- 9:00 Introduction to the Teraflop-Workbench (M.Resch, HLRS)
- 9:15 The new NEC systems at HLRS (H. Berger, NEC) 
- 10:00 Coffee
- 10:15 Differences SX-6/SX-8, Basics on Vectorization (M. Galle et al., NEC) 
- 11:15 Coffee
- 11:00 Vectorization and optimization examples (M. Galle et al., NEC) 
- 12:30 Lunch
- 13:30 Indirect addressing, General Strategy for Code Tuning (M. Galle et al., NEC)
- 15:00 Coffee 
- 15:15 Most important compiler switches F90/C++,  
how to use OpenMP and MPI on SX (S. Borowski et al., NEC) 
- 16:30 End






Introduction Rolf Rabenseifner  
Slide 1 (SX-8) Höchstleistungsrechenzentrum Stuttgart

HLRS 

## NEC SX-8 Course — 2nd day (Thursday, April 7, 2005)

### Filesystems and MPI-2 parallel I/O on NEC SX-8

- 9:00 Filesystems, file import/export, parallel I/O on NEC SX-8 (H.Berger, NEC)
- 9:45 MPI-2 parallel file I/O (basics)  (R. Rabenseifner, HLRS)
- 10:45 Coffee
- 11:00 MPI-2 parallel file I/O (fileviews)  (R. Rabenseifner, HLRS)
- 12:00 MPI-2 parallel file I/O (access methods)  (R. Rabenseifner, HLRS)
- 13:00 End



Introduction Rolf Rabenseifner  
Slide 2 (SX-8) Höchstleistungsrechenzentrum Stuttgart

HLRS 